

IN THE CLAIMS:

Please cancel claims 1 - 20.

Please add the following new claims 21 - 23.

21. A microcomputer comprising:
- an electrically programmable ROM which has a first region to store a user program therein and a second region to store data therein;
- a memory which stores a write control program for a writing to the electrically programmable ROM; and
- a CPU which executes the user program and the write control program, wherein the electrically programmable ROM and the memory are disposed at mutually different address positions,
- wherein the user program includes a first instruction which changes a process of the CPU executing the user program to a process that controls a writing of the electrically programmable ROM based on the write control program stored in the memory, and
- wherein the write control program includes a second instruction which returns the CPU to the process based on the executing of the user program stored in the electrically programmable ROM after completion of the process that controls the writing of the electrically programmable ROM.

22. A microcomputer according to claim 21,  
wherein the memory which stores a write control program is a mask ROM.

23. A microcomputer according to claim 21,  
wherein the memory which stores a write control program is a RAM that receives the write control program from the electrically programmable ROM.

REMARKS

The foregoing amendments are being submitted to place the subject application in better condition for examination. It is believed that no new matter has been added to this application. Early favorable action is hereby respectfully requested.